



Atty. Dkt. No. 023312-0109

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Takashi NAGASE et. al.
Title: METHOD OF MANUFACTURING NANO-
GAP ELECTRODE
Appl. No.: 10/662,886
Filing Date: 09/16/2003
Examiner: Ahmed, Shamim
Art Unit: 1765

TRANSMITTAL OF FORMAL DRAWING

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith is the formal drawing (1 sheet, Figure 1) for the above-identified application. The Official Draftsperson is respectfully requested to approve this drawing for entry into the application.

Respectfully submitted,

Date December 7, 2005

By 

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